

Student Seminar

Guideline for the Written Seminar Report

Most of the hints in this document are meant as recommendations. However, if you would like to deviate from them, you should have good reasons and should use your style consistently.

Formal

- Length of the report: 15 to 20 pages, DIN A4, single sided.

Title page

- Seminar (course), semester, author, title, advisor.
- No page number.

Table of contents

- Numbering: use decimal digits (1, 1.1, 1.1.1, ...)
- Heading should carry information (not just "Beginning", "Main Part", "End")
- Heading in the same hierarchy level (1.1 and 1.2) should correspond to an equivalent logical rank
- Heading should be different from the main title and subheadings should be different from heading etc.

Main part

- Summarize the important information in your own words. Do not simply copy the contents of your reference literature.
- Explain special terms and abbreviations before usage.
- Statements have to be either proofed by yourself, or a reference must be provided.
- Use abbreviations for references, for example:
... Several schemes have been proposed for detecting these faults by applying weighted random patterns [BRGL89b, Wu87, StWu91], pseudo-exhaustive patterns [Akers85, HWH90] or deterministic patterns [Kone91, HELL92, HELL95, ToMc96, WuKi96, ZRTW96, KiWu97]. Most of the deterministic schemes are designed for single scan path architectures. Multiple scan chains are addressed in the schemes of [Kone91] and [ZRTW96] which are based on encoding deterministic patterns by seeds for an LFSR, ...
- Citations have to be identified. If you omit or add anything, make it clearly visible.

- Number figures and tables and provided captions (below the respective table/figure). The main text should reference to and describe the table or figure. For example:

...The target structure is shown in figure 4. It consists of an LFSR and a combinational function, the Sequence-Generating Logic (SGL). The SGL passes the bit sequences generated by the LFSR to the scan paths, and modifies these sequences at certain bit positions which are selected by both the state of the control unit and the state of the LFSR.

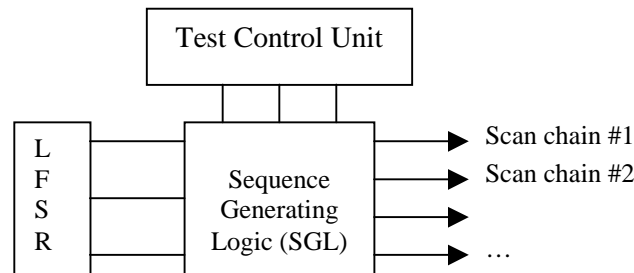


Figure 4: Target Structure

References

- Include all references you cited (and only those).
- Sort them alphabetically.
- Include the abbreviation together with the author, title, origin and year of publication, for example:

Bray84 R. K. Brayton, G. D. Hachtel, C. McMullen, A. Sangiovanni: "Logic Minimization Algorithms for VLSI Synthesis", Boston: Kluwer Academic Publishers, 1984.

Brgl85 F. Brglez, H. Fujiwara: "A Netral Netlist of 10 Combinational Benchmark Circuits and Target Translation in Fortran", Proc. Of International Symp. On Circuits and Systems, 1985, pp. 663-698.

Notes

Everybody tends to overlook simple errors after delving deeply into a subject (including type errors, incorrect grammar) in his/her own report. In order to avoid these types of errors it is useful to give the report to a colleague or friend for proofreading.